

USB-67230/67250

USB 2.0-based Digital I/O Module

User's Manual



Manual Rev.: 1.00

Revision Date: Aug. 3, 2017

Advance Technologies; Automate the World.



Preface

Copyright

This document contains proprietary information protected by copyright. All rights are reserved. No part of this manual may be reproduced by any mechanical, electronic, or other means in any form without prior written permission of the manufacturer. All specifications are subject to change without further notice.

Disclaimer

The information in this document is subject to change without prior notice in order to improve reliability, design, and function and does not represent a commitment on the part of the manufacturer.

In no event will the manufacturer be liable for direct, indirect, special, incidental, or consequential damages arising out of the use or inability to use the product or documentation, even if advised of the possibility of such damages.

Environmental Responsibility

JYTEK is committed to fulfill its social responsibility to global environmental preservation through compliance with the European Union's Restriction of Hazardous Substances (RoHS) directive and Waste Electrical and Electronic Equipment (WEEE) directive. Environmental protection is a top priority for JYTEK. We have enforced measures to ensure that our products, manufacturing processes, components, and raw materials have as little impact on the environment as possible. When products are at their end of life, our customers are encouraged to dispose of them in accordance with the product disposal and/or recovery programs prescribed by their nation or company.

Conventions

Take note of the following conventions used throughout this manual to make sure that users perform certain tasks and instructions properly.

Preface iii





Additional information, aids, and tips that help users perform tasks.



Information to prevent *minor* physical injury, component damage, data loss, and/or program corruption when trying to complete a task.



Information to prevent *serious* physical injury, component damage, data loss, and/or program corruption when trying to complete a specific task.

iv Preface



Table of Contents

Pr	eface	•••••		iii
Ta	ble o	f Con	tents	v
Lis	st of F	igure	s	vii
Lis	st of T	ables	S	ix
1	Intro	ducti	ion	1
	1.1	Ove	rview	1
	1.2	Feat	tures	1
	1.3	Арр	lications	2
	1.4	Spe	cifications	2
	1.	.4.1	General Specifications	3
	1.	.4.2	Digital Input (DI)	3
	1.	.4.3	Digital Output (DO)	4
	1.5	Soft	ware Support	4
	1.6	Driv	ver Support for Windows	5
	1.7	Utili	ities for Windows	6
	1.8	Sch	ematics and Dimensions	7
	1.	.8.1	Module	7
	1.	.8.2	Module Stand	10
	1.	.8.3	Rail Mounting	16
	1.	.8.4	Wall Mounting	20
	1.9	Con	nector Pin Assignment	22
2	Gett	ing St	tarted	25
	2.1	Unp	packing Checklist	25
	2.2	Con	necting the USB-67230/67250 Module	25
	2.3	Dev	rice ID	26

Table of Contents



3	Opera	ations	29		
	3.1	Isolated Digital Input	30		
	3.2	Change of State (COS) Detection	31		
	3.3	Optical Isolated Frequency/Event Counter	33		
	3.4	Digital Filtering	35		
	3.5	Isolated Digital Output (USB-67230 only)	36		
	3.6	Relay Output (USB-67250 only)	37		
lm	portai	nt Safety Instructions	39		
<i>.</i>					
W	Warranty Policy 4				
Ge	Getting Service				

vi Table of Contents



List of Figures

Figure 1-1:	USB-67230/67250 Module Rear View	7
Figure 1-2:	USB-67230/67250 Module Side View	8
Figure 1-3:	USB-67230 Module Front View	9
Figure 1-4:	USB-67250 Module Front View	10
Figure 1-5:	Module, Stand, Connector, and USB Cable	11
Figure 1-6:	Module, Stand, & Wall Mount Kit Side View (w/ cor 12	
Figure 1-7:	Module in Stand Front View	13
Figure 1-8:	Module Stand Top View	14
Figure 1-9:	Module Stand Side Cutaway View	15
Figure 1-10:	Module Stand Front View	16
Figure 1-11:	Rail Mount Kit	17
Figure 1-12:	Module Pre-Rail Mounting	18
Figure 1-13:	Module Rail-Mounted	
Figure 1-14:	Wall Mount Holes	20
Figure 1-15:	Module with Wall Mount Apparatus	21
Figure 2-1:	Device ID Selection Control	27
Figure 3-1:	USB-67230 Functional Block Diagram	29
Figure 3-2:	USB-67250 Functional Block Diagram	30
Figure 3-3:	USB-67230 Isolated Input	31
Figure 3-4:	USB-67250 Isolated Input	31
Figure 3-5:	COS Detection Architecture	32
Figure 3-6:	COS Example	33
Figure 3-7:	USB-67230/67250 Optical Isolated Frequency/Even 33	t Counter
Figure 3-8:	Frequency Counter Example	34
Figure 3-9:	Frequency Counter Error %	35
Figure 3-10:	Digital Filter Example	36
Figure 3-11:	USB-67230 Isolated Output	37
Figure 3-12:	Form C Relay	
Figure 3-13:	Form A Relay	37

List of Figures vii



List of Tables

Table 1-1:	USB-67230 Pin Assignment	22
Table 1-2:	USB-67230 I/O Signal Description	23
Table 1-3:	USB-67250 Pin Assignment	24
Table 1-4:	USB-67250 I/O Signal Description	24

List of Tables ix



1.1 Overview

The USB-67230/67250 USB-based digital I/O modules feature high voltage ON/OFF control and monitoring, and isolation voltage supported up to 2500V_{RMS}. The USB-67230 provides 32-channel isolated digital I/O and 2-channel frequency/event counters, and USB-67250 provides 8-channel isolated digital inputs, 8-channel electromechanical relays, and 2-channel frequency/event counters.

The USB-powered USB-67230/7250 have removable screw-down terminals for easy device connectivity, and the included multi-functional stand fully supports desktop, rail, or wall mounting.

The USB-67230/67250 are suitable for industrial I/O control applications requiring high voltage and excellent protection. High isolation voltage protects against damage from accidental contact with high external voltage and eliminates troublesome ground loops. U-Test, a free ready-to-use testing program, is included to enable operation or testing of all JYTEK USB DAQ series functions with no programming requirement.

1.2 Features

- ► High-speed USB 2.0
- USB-powered
- ▶ USB-67230: 16CH isolated DI, 16CH isolated DO, 2CH frequency/event counters
- USB-67250: 8CH electromechanical relays, 8CH isolated DI, 2CH frequency/event counters
- ► Programmable digital filter removing unexpected glitches on input channels
- Programmable initial DO status
- ► Up to 2500V_{RMS} isolation voltage
- Removable screw-down terminal on module
- ► Lockable USB cable for secure connectivity
- ► Ready-to-use testing application (U-Test) included



1.3 Applications

- ► Automotive testing
- ► Laboratory research
- ► Industrial I/O control
- ► Signal switching

1.4 Specifications



1.4.1 General Specifications

Physical, Power, and Operating Environment				
Interface	High speed USB 2.0 compatible, mini-USB connector			
Dimensions	156.5 (L) x 114 (W) x 41.3 (H) mm (6.16 X 4.49 X 1.63 in.)			
I/O Connector	Two 20-pin removable screw-down terminals			
Power requirement	USB power (5 V @ 400 mA)			
Operating environment	Ambient temperature: 0 to 55°C Relative humidity: 10% to 90%, non-condensing			
Storage environment	Ambient temperature: -20 to 70 °C Relative humidity: 5% to 95%, non-condensing			

1.4.2 Digital Input (DI)

	USB-67230	USB-67250	
Optical Isolated Input			
Number of channels	16	8	
Polarity	Bi-directional	(non-polarity)	
Logic level	VIH=5~24V, Or dry		
Input resistance	2.4k @	0.5W	
Isolated voltage	2500V _{RMS} (channel to system)		
Min. pulse width for change of state (COS) detection	20.83 ns (software programmable)		
Data transfer	Programmed I/O		
Optical Isolated Frequency/Eve	nt Counter		
Number of channels	2		
Logic level	VIH=5~12V, VIL=0~1.5V		
Event counter width	32-bit		
Max. input frequency (DC coupled)	1 MHz		
Min. input frequency (DC coupled)	0.1 Hz		



	USB-67230	USB-67250	
Max. frequency error	0.5% (f≤50kHz) 1% (50kHz <f≤500khz) 2% (500kHz<f≤1mhz)< td=""></f≤1mhz)<></f≤500khz) 		
Data transfer	Programmed I/O		

1.4.3 Digital Output (DO)

USB-67230

Channels	16 (optical isolation)		
Output type	Open drain MOSFET		
Supply voltage	5-35VDC		
Max. sink current	250 mA @ 100% duty (/channel)		
Data transfer	Programmed I/O		

USB-67250

Channels	8 (electromechanical relay, non-latching)		
Contact rating	Max. switching power	60 W, 125 VA	
(/channel)	Max. switching voltage	220 VDC, 250VAC	
	Max. switching current	2 A	
Relay ON/OFF time	Operate time 2 ms		
	Release time	1 ms	
Contact resistance	75mΩ (max.)		
Expected lifetime	50 VDC 0.1A (resistive), 1x10 ⁶ operations		
Breakdown voltage	1000 VAC		
Data transfer	Programmed I/O		

1.5 Software Support

JYTEK provides comprehensive software drivers and packages to suit various user approaches to system building. In addition to programming libraries, such as DLLs, for most Windows-based systems, JYTEK also provides drivers for other application environments. JYTEK also provides ActiveX component ware for measurement and SCADA/HMI, and break-



through proprietary software. All software options are on the website: www.jytek.com.

Be sure to install the driver & utility before use.

1.6 Driver Support for Windows

UD-DASK

UD-DASK is composed of advanced 32/64-bit kernel drivers for customized DAQ application development. USB-DASK enables you to perform detailed operations and achieve superior performance and reliability from the data acquisition system. DASK kernel drivers now support Windows 7/Vista® OS.



Only UD-DASK versions 1.0.5 and later support the USB-67230/67250 module.

DAQPilot

DAQPilot is a SDK with a graphics-driven interface for various application development environments. DAQPilot represents JYTEK's commitment to full support of its comprehensive line of data acquisition products and is designed for novice to most experienced programmers. As a task-oriented DAQ driver, SDK and wizard for Windows systems, DAQPilot helps shorten development time while accelerating the learning curve for data acquisition programming.

Download and install DAQPilot from:

http://www.jytek.com



Only UD-DASK versions 2.3.4.1109 and later and later support the USB-67230/67250 module.



1.7 Utilities for Windows

U-Test

U-Test is a free and ready-to-use utility assisting instant testing and operation of all JYTEK USB DAQ product functions with no programming requirment. In addition to data collection and monitoring functions, U-Test also supports basic FFT analysis and direct control of analog output and digital I/O with a user-friendly interface.

Download and install U-Test from:

http://www.jytek.com

1.8 Schematics and Dimensions



All dimensions shown are in millimeters (mm)

1.8.1 **Module**





Figure 1-1: USB-67230/67250 Module Rear View

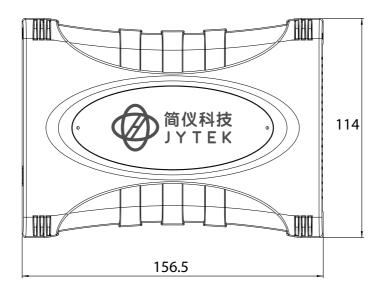


Figure 1-2: USB-67230/67250 Module Side View



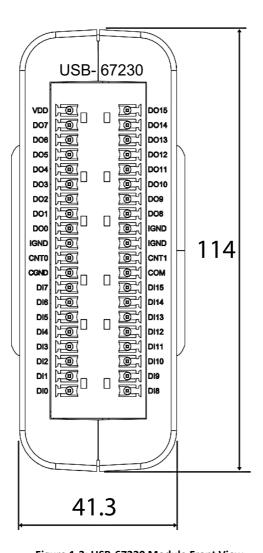


Figure 1-3: USB-67230 Module Front View



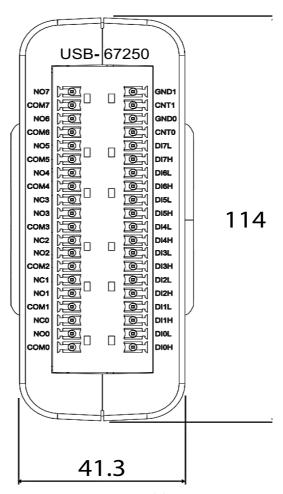


Figure 1-4: USB-67250 Module Front View

1.8.2 Module Stand

The multi-function USB-67230/67250 stand is compatible with desk, rail, or wall mounting. To fix the module in the stand, slide the module body into the stand until a click is heard. To remove the module from the



stand, twist the bottom of the stand in a back-and forth motion and separate from the module.

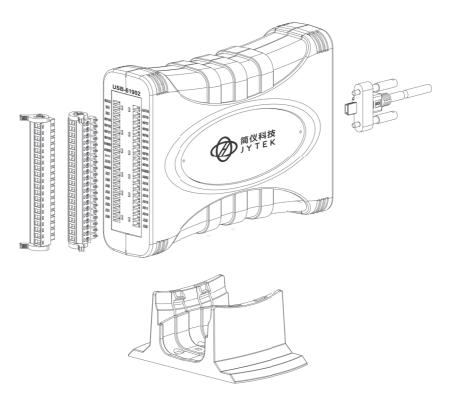


Figure 1-5: Module, Stand, Connector, and USB Cable



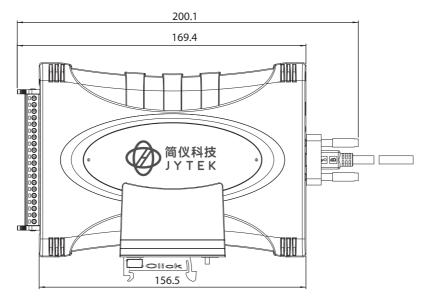


Figure 1-6: Module, Stand, & Wall Mount Kit Side View (w/ connections)



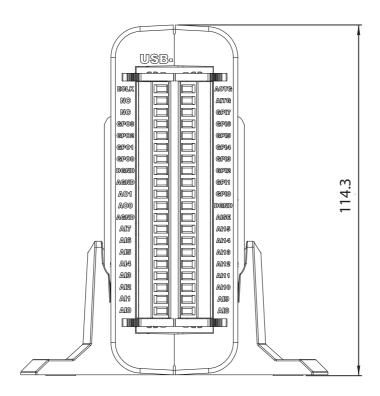


Figure 1-7: Module in Stand Front View



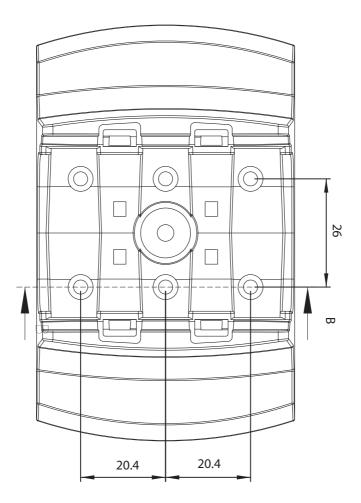


Figure 1-8: Module Stand Top View



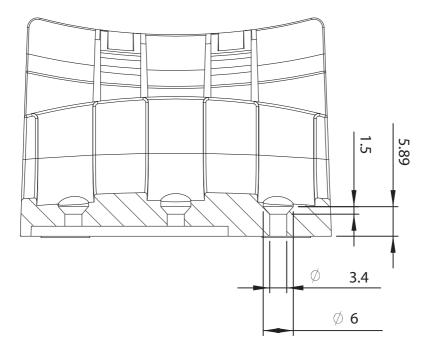


Figure 1-9: Module Stand Side Cutaway View



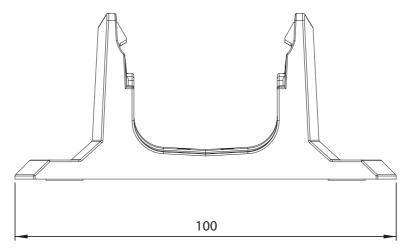


Figure 1-10: Module Stand Front View

1.8.3 Rail Mounting

The multi-function stand can be mounted on the DIN rail using the rail-mount kit as shown.



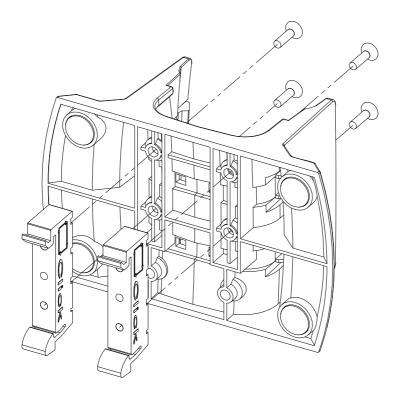


Figure 1-11: Rail Mount Kit



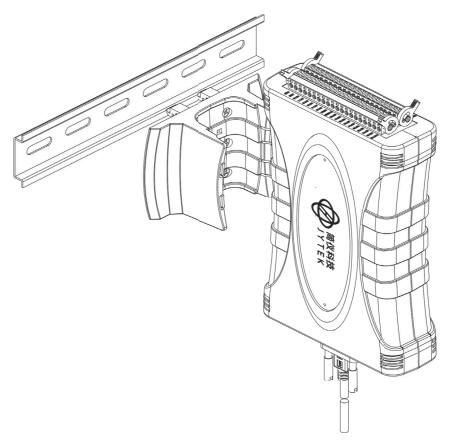


Figure 1-12: Module Pre-Rail Mounting



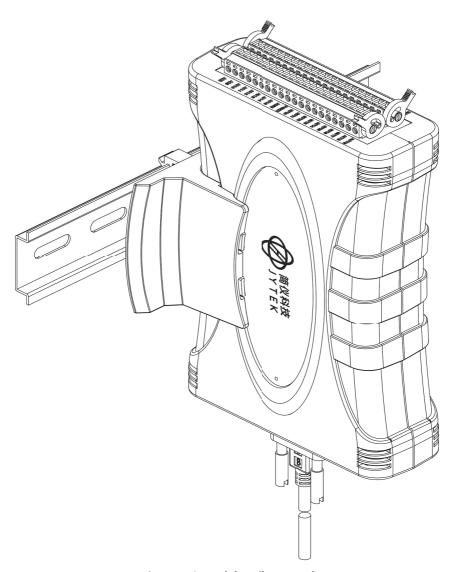


Figure 1-13: Module Rail-Mounted



1.8.4 Wall Mounting

The multi-function stand can be fixed to a wall using four flush head screws as shown. The four screw holes should be approximately 3.4 mm in diameter.

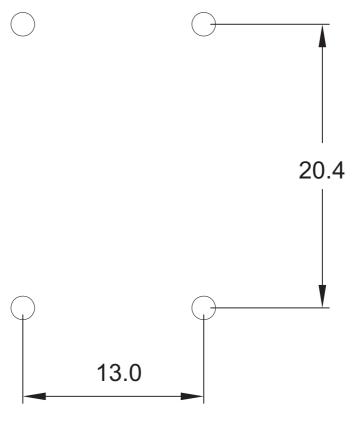


Figure 1-14: Wall Mount Holes



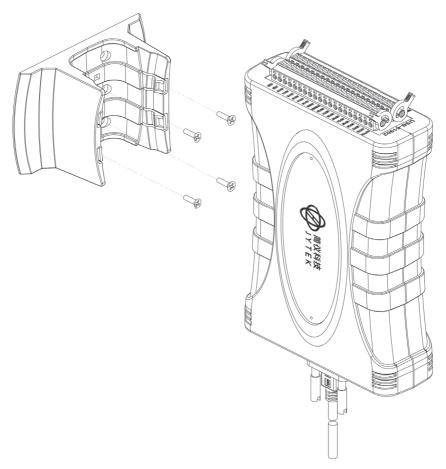


Figure 1-15: Module with Wall Mount Apparatus



1.9 Connector Pin Assignment

The USB-67230/67250 module is equipped with 40-pin removable screw-down terminal connectors, with pin assignment as follows.

Pin	Function	Pin	Function
20	VDD	40	DO15
19	DO7	39	DO14
18	DO6	38	DO13
17	DO5	37	DO12
16	DO4	36	DO11
15	DO3	35	DO10
14	DO2	34	DO9
13	DO1	33	DO8
12	DO0	32	IGND
11	IGND	31	IGND
10	CNT0	30	CNT1
9	CGND	29	СОМ
8	DI7	28	DI15
7	DI6	27	DI14
6	DI5	26	DI13
5	DI4	25	DI12
4	DI3	24	DI11
3	DI2	23	DI10
2	DI1	22	DI9
1	DI0	21	DI8

Table 1-1: USB-67230 Pin Assignment

Signal Name	Reference	Direction	Description
DI<015>	СОМ	I	Isolated digital input channel



Signal Name	Reference	Direction	Description
СОМ			Common ground or common power of isolated digital input channel
CNT<0,1>	CGND	I	Frequency/event counter channel
CGND			Ground of Frequency/event counter
DO<015>	IGND	0	Isolated digital output channel
IGND			Ground return path for isolated output channel
VDD	IGND	I	Common power input junction for isolated output channel

Table 1-2: USB-67230 I/O Signal Description

Pin	Function	Pin	Function
20	NO7	40	GND1
19	COM7	39	CNT1
18	NO6	38	GND0
17	COM6	37	CNT0
16	NO5	36	DI7L
15	COM5	35	DI7H
14	NO4	34	DI6L
13	COM4	33	DI6H
12	NC3	32	DI5L
11	NO3	31	DI5H
10	COM3	30	DI4L
9	NC2	29	DI4H
8	NO2	28	DI3L
7	COM2	27	DI3H
6	NC1	26	DI2L
5	NO1	25	DI2H
4	COM1	24	DI1L



Pin	Function	Pin	Function
3	NC0	23	DI1H
2	NO0	22	DI0L
1	СОМ0	21	DI0H

Table 1-3: USB-67250 Pin Assignment

Signal Name	Reference	Direction	Description
COM<07>		I	Common pin of relay <07>
NO<07>	COM<07>	I	Normal open pin of relay <07>
NC<03>	COM<03>	I	Normal close pin of relay <03>
DI<07>H	DI<07>L	I	High input of isolated differential digital input
DI<07>L		I	Low input of isolated differential digital input
CNT<0,1>	GND<0,1>	I	Frequency/event counter channel
GND<0,1>			Ground of Frequency/event counter

Table 1-4: USB-67250 I/O Signal Description



2 Getting Started



The appropriate driver must be installed before connection to the computer system. See Section 1.5: Software Support for driver support information.

2.1 Unpacking Checklist

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform the dealer immediately. Retain the shipping carton and packing materials for inspection. Obtain authorization from the dealer before returning any product to JYTEK. Ensure that the following items are included in the package.

- ▶ USB-67230/67250
- Stand
- ► Two removable screw terminals
- ▶ USB cable (2-meter length)
- Railmount kit

2.2 Connecting the USB-67230/67250 Module

- 1. Turn on the computer.
- 2. Connect the USB-67230/67250 module to one USB 2.0 port on the computer using the included USB cable.
- The first time the USB-67230/67250 module is connected, a New Hardware message appears. It will take a few seconds to load the firmware. When loading is complete, the LED indicator on the rear of the USB DAQ module changes from amber to green and the New Hardware message closes.
- 4. The USB-67230/67250 module can now be located in the hardware Device Manager.

Getting Started 25





The USB-67230/67250 module is exclusively powered by the USB port and requires 400 mA @ 5 V. If the USB-67230/67250 module cannot be detected, power provided by the USB port may be insufficient.

2.3 Device ID

A rotary control on the rear of the module (as shown) controls device ID setting and can be set from 0 to 7. The device ID allows dedicated control of the USB-67230/67250 module irrespective of the connected USB port. When more than one USB module of the same type is connected, each must be set to a different ID to avoid conflicts and errors in operation.

26 Getting Started



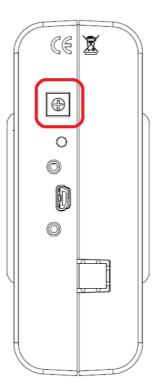


Figure 2-1: Device ID Selection Control

All remaining hardware configuration is software programmable, including sampling/update rate, input/output channel, input range, and others. Please see the UD-DASK Function Reference manual for details.

Getting Started 27



3 Operations

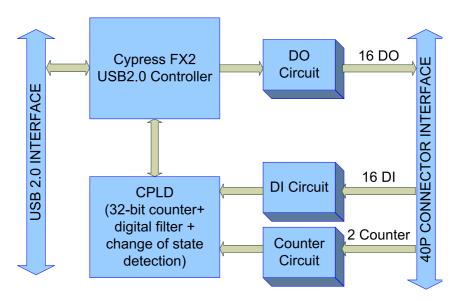


Figure 3-1: USB-67230 Functional Block Diagram

The USB-67250 provides 8CH optical isolation digital inputs, 8CH relay outputs (4CH form C and 4CH form A), and 2CH frequency/event counters.



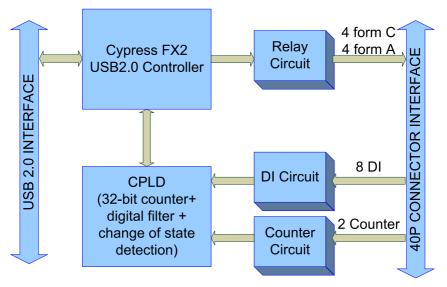


Figure 3-2: USB-67250 Functional Block Diagram

3.1 Isolated Digital Input

The USB-67230/67250 support 16 or 8 opto-isolated input channels, as follows, with digital input first routed through a photo-coupler (PC3H4), and normal input voltage range for high state from 5 to 24V.



For USB-67230, all digital inputs share the same common junction (COM), with connections either common power or common ground, and with USB-67250, each input channel has an individual differential input pair, preventing connections from being polarity sensitive, irrespective of the connected voltage.



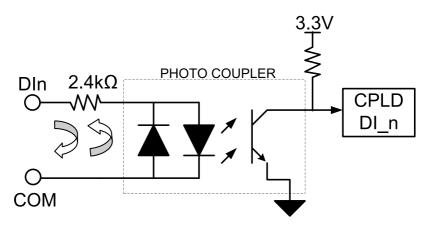


Figure 3-3: USB-67230 Isolated Input

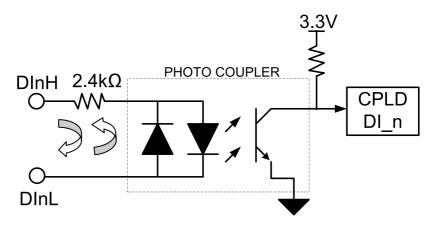


Figure 3-4: USB-67250 Isolated Input

3.2 Change of State (COS) Detection

COS (Change of State) refers to input state (logic level) changing from low to high or vice versa, wherein the COS detection circuit registers the edge of the level change.



In the USB-67230/67250, the COS detection circuit is applied to all DI channels, with the channel(s) to enable COS detection selectable by software. When an enabled channel changes logic level, the COS detection circuit generates an interrupt request to the USB microcontroller, which, when detected, latches corresponding DI data into the COS latch register. In COS architecture, DI data is sampled by a 48 MHz base clock, such that pulse width of the digital input exceeds 21 ns, or the COS latch register cannot latch the correct input data. The COS latch register is cleared when the register is read out, resuming availability to latch the susequent COS.



Maximum frequency of COS detection depends on software latency and computer performance, and is not guaranteed if COS frequency exceeds 1 kHz.

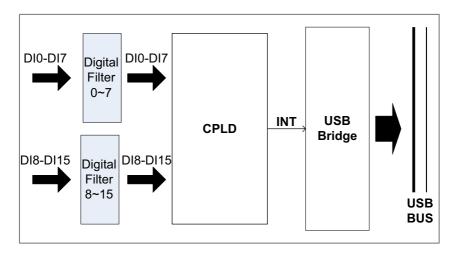


Figure 3-5: COS Detection Architecture



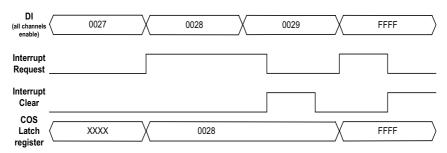


Figure 3-6: COS Example

3.3 Optical Isolated Frequency/Event Counter

Calculates the number of rising or falling edges occurring on the input channel, with counter width of 32 bits counting up from 0. The polarity (rising or falling edge) of valid events is software configurable.

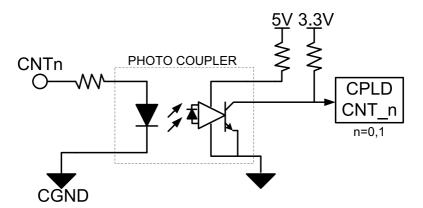


Figure 3-7: USB-67230/67250 Optical Isolated Frequency/Event Counter

The frequency counter base clock is 48MHz. The frequency counter calculates base clocks occurring within a period (rising edge to rising edge or falling edge to falling edge) of the repetitive input signal, which is then converted to frequency value. Counter polarity can be adjusted to rising edge active or falling edge active.

The following example shows frequency measurement of a 1 MHz signal by counter0 with rising-edge polarity and 500 kHz signal by counter1



with falling-edge polarity. Counter value is updated and reset in every period.

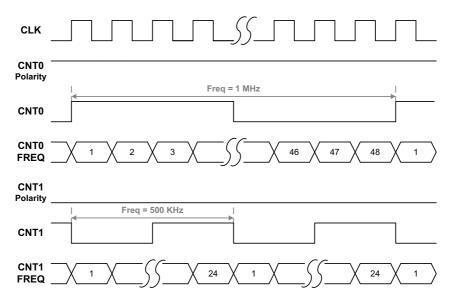


Figure 3-8: Frequency Counter Example

Since the signals are sampled by a 48MHz base clock, latch timing can generate measurement error, maximum error ratio vs. input frequency is as follows.



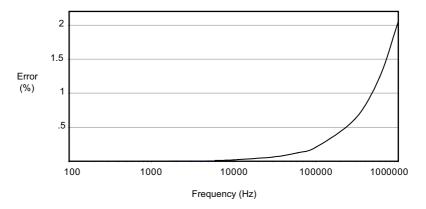


Figure 3-9: Frequency Counter Error %

3.4 Digital Filtering

Filters unexpected glitch signals from the input channels. By default, when enabled, the input channel ignores signal changes from one state to another when not remaining in the state for a pre-defined period. The digital filter function is applied on all DI channels and counters, with the default setting disabled. When enabled by software, it is necessary to configure the minimum pulse-width value. This value represents the minimum period of time guaranteed to pass through the filter when the signal changes. The digital filter uses an internal 16-bit counter to define the specified filter value. Data is sampled by a 48 MHz base clock, with minimum pulse-width value a multiple of 20.83 ns and multiple number from 1 to 65535, representing the minimum pulse-width from 20.83 ns to 1.365 ms.

In an examplary digital filter operation, as shown, data is sampled by a 48 MHz base clock, and filter stage is 10, such that minimum pulse width value is 208.3 ns. The pulse width of the first two signals is 180 ns, shorter than the specified filter value 208.3 ns. Accordingly, the first two pulses are ignored, and the next two signals, with pulse width of 230 ns (longer than the specified filter value 208.3 ns) are recognized. When pulse width of last two signals is 180 ns when filter is disabled, definitely, the last two states are recognized.



filter_out

Figure 3-10: Digital Filter Example

To reject a signal deviating from a state for the specified period of time (minimum pulse width), the filter stage must be set to agree with: 20.83 ns × filter stage < minimum pulse Width (ns).

3.5 Isolated Digital Output (USB-67230 only)

As shown, when isolated digital output is ON, sink current is conducted through the power MOSFET, and when OFF, no current flows through the power MOSFET. When the load is of an "inductance nature" such as a relay, coil or motor, the VDD pin must be connected to an external power source, in order for the flywheel diode to form a current-release closed loop, protecting the power MOSFET from high reverse voltage generated by the inductance load when the output is switched. The DO output status is saved in the USB microcontroller and can be read back if necessary. The USB-67230 also features programmable power-up output status, allowing output in a known state when powered on. When the module is powered off (ejected from the USB port), all digital output reverts to OFF.

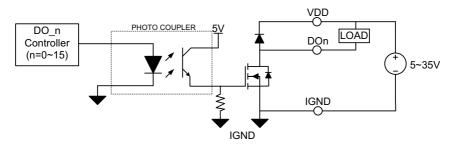
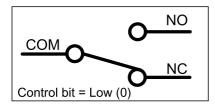


Figure 3-11: USB-67230 Isolated Output



3.6 Relay Output (USB-67250 only)

USB-67250 provides Form C and Form A relays, with channels 0 to 3 Form C, and 4 to 7 form A, as shown.



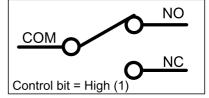
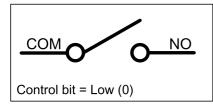


Figure 3-12: Form C Relay



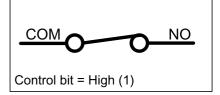


Figure 3-13: Form A Relay

Form C relays have NC (Normal Close), NO (Normal Open), and COM (Common) contacts. The COM terminal, located in the center,must contact either the NO pole or NC pole. When the control bit is high (1), contact is made between the COM terminal and NO pole, and if low (0), between the COM terminal and NC pole. Form A relays have only NO (Normal Open) and COM (Common) contacts. When the control bit is high (1), the COM terminal znd NO pole contact. If the control bit is low (0), no contact takes place. Current relay output status can be read back by software, and during normal power up, reset, and power down (ejection from USB port), the relay is low status. In addition, USB-67250 also features programmable power-up output status, providing output state when powered up.



Important Safety Instructions

For user safety, please read and follow all instructions, Warnings, Cautions, and Notes marked in this manual and on the associated device before handling/operating the device, to avoid injury or damage.

S'il vous plaît prêter attention stricte à tous les avertissements et mises en garde figurant sur l'appareil, pour éviter des blessures ou des dommages.

- ▶ Read these safety instructions carefully
- ► Keep the User's Manual for future reference
- ► Read the Specifications section of this manual for detailed information on the recommended operating environment
- ► The device can be operated at an ambient temperature of 50°C
- ► When installing/mounting or uninstalling/removing device; or when removal of a chassis cover is required for user servicing (See "Getting Started" on page 25.):

 - ▷ Reinstall all chassis covers before restoring power
- ► To avoid electrical shock and/or damage to device:

 - Always install and operate device near an easily accessible electrical outlet
 - Secure the power cord (do not place any object on/over the power cord)
 - Only install/attach and operate device on stable surfaces and/ or recommended mountings
- ► If the device will not be used for long periods of time, turn off and unplug from its power source
- ► Never attempt to repair the device, which should only be serviced by qualified technical personnel using suitable tools



► A Lithium-type battery may be provided for uninterrupted backup or emergency power.



Risk of explosion if battery is replaced with one of an incorrect type; please dispose of used batteries appropriately.

Risque d'explosion si la pile est remplacée par une autre de type

► The device must be serviced by authorized technicians when:

incorrect. Veuillez jeter les piles usagées de façon appropriée.

- ▷ Liquid has entered the device interior
- The device has been exposed to high humidity and/or moisture
- The device is not functioning or does not function according to the User's Manual
- ▶ Disconnect the power supply cord before loosening the thumbscrews and always fasten the thumbscrews with a screwdriver before starting the system up
- ► It is recommended that the device be installed only in a server room or computer room where access is:
 - Restricted to qualified service personnel or users familiar with restrictions applied to the location, reasons therefor, and any precautions required
 - Only afforded by the use of a tool or lock and key, or other means of security, and controlled by the authority responsible for the location





BURN HAZARD

Touching this surface could result in bodily injury. To reduce risk, allow the surface to cool before touching.

RISQUE DE BRÛLURES

Ne touchez pas cette surface, cela pourrait entraîner des blessures.

Pour éviter tout danger, laissez la surface refroidir avant de la toucher.



Warranty Policy

Thank you for choosing JYTEK. To understand your rights and enjoy all the after-sales services we offer, please read the following carefully.

- 1. All JYTEK products come with a limited two-year warranty:
 - The warranty period starts on the day the product is shipped from JYTEK's factory.
 - ▶ Peripherals and third-party products not manufactured by JYTEK will be covered by the original manufacturers' warranty.
 - For products containing storage devices (hard drives, flash cards, etc.), please back up your data before sending them for repair. JYTEK is not responsible for any loss of data.
 - Please ensure the use of properly licensed software with our systems. JYTEK does not condone the use of pirated software and will not service systems using such software. JYTEK will not be held legally responsible for products shipped with unlicensed software installed by the user.
 - ▷ For general repairs, please do not include peripheral accessories. If peripherals need to be included, please contact JYTEK.

Warranty Policy 43



- 2. Our repair service is not covered by JYTEK's guarantee in the following situations:
 - Damage caused by not following instructions in the User's Manual.
 - Damage caused by carelessness on the user's part during product transportation.
 - Damage caused by fire, earthquakes, floods, lightening, pollution, other acts of God, and/or incorrect usage of voltage transformers.
 - Damage caused by unsuitable storage environments (i.e. high temperatures, high humidity, or volatile chemicals).
 - Damage caused by leakage of battery fluid during or after change of batteries by customer/user.
 - Damage from improper repair by unauthorized JYTEK technicians.
 - Products with altered and/or damaged serial numbers are not entitled to our service.

 - Other categories not protected under our warranty.
- 3. Customers are responsible for shipping costs to transport damaged products to our company or sales office.

If you have any further questions, please email our service center: service@jytek.com.



北京海洋兴业科技股份有限公司(证券代码: 839145)

北京市西三旗东黄平路19号龙旗广场4号楼(E座)906室 电话: 010-62176775 62178811 62176785 邮编: 100096

传真: 010-62176619 邮箱: market@oitek.com.cn 企业官网: www.hyxyyq.com 购线网: www.gooxian.com







微信公众号

44 Warranty Policy